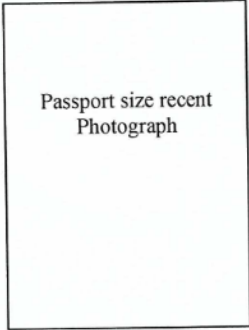# DEPARTMENT OF ELECTRICAL ENGINEERING
ZAKIR HUSAIN COLLEGE OF ENGINEERING AND TECHNOLOGY
ALIGARH MUSLIM UNIVERSITY, ALIGARH

APPLICATION FORM FOR THE POST OF SRF/RA IN THE CRGS PROJECT FUNDED BY CSC, UNSW-AUSTRALIA

INSTRUCTIONS TO THE CANDIDATE:

1. All the applications duly filled must be emailed on tariq.ee@zhcet.ac.in (soft copy) latest by **11:59 PM on 11.07.2020 (Saturday).**
2. No application will be entertained after the last date.
3. No TA/DA will be provided to the candidate.
4. Candidates already in job must bring the No objection certificate from their employer on the day of interview.
5. The PI in consultation with the funding agency of the project reserves the right to call or not to call any candidate for the interview. The summoning of candidates for interview, conveys no assurance whatsoever that they will be selected.
6. The selected candidate/s will be intimated through their email. No correspondence will be made with candidates who are not selected.

Passport size recent Photograph

APPLICATION FORM

1. Post applied for (SRF/RA) : ....

2. Candidate's Name in full (Dr./Mr./Mrs./Miss): ....(In Capital Letters)

3. (a) Father's Name: ....(b) Mother's Name: ....

4. Address:

Permanent:	Correspondence (Leave blank if same as permanent):
Email:	Mobile No:

5. Date of Birth (in Christian era) : ..../........../.............. (dd/mm/yyyy)

6. (a) Marital Status: .... (b) Nationality: ....

7. Educational Qualifications (from highest to lowest):

Sl. No.	Examination	Subject	Percentage	Univ./Board	Year of Passing

8. Title of PhD Thesis:...
....

9. Status of PhD (Awarded/Submitted/ for currently pursuing write in which year registered) :............................
....

10. Title of M. Tech Dissertation/Thesis: ....
....

11. Details of Research papers published/accepted/submitted in (**SCI Indexed Journals**) only:

S. No.	Title of paper	Transaction/Journal	IF	Year
1.				
2.				
3.				

Kindly attach a separate sheet if the number of papers is more than 3.

12. Patents:
    a. Granted: ....
    b. Published: ....

13. For SRF, write few lines (if any) on your experience with Real-time emulators/hardware in loop (Typhoon HIL, PLECS, OPAL-RT, etc):

....

....

....

....

14. For RA, write few lines (if any) on your experience with machine learning/deep learning technique applied in electrical engineering domain):

....

....

....

....

I ...., hereby solemnly declare that the entries made by me in the above form are true to the best of my knowledge. If any entry is found incorrect, the form may be rejected.

Signature of the candidate (in full)

Date and Place:..