



भारतीय ताराभौतिकी संस्थान INDIAN INSTITUTE OF ASTROPHYSICS

(विज्ञान व प्रौद्योगिकी विभाग, भारत सरकार के अधीन स्वायत्त संस्थान)

(An Autonomous Body Under Department of Science & Technology, Government of India)

कोरमंगला Koramangala, बेंगलूर BENGALURU -560034

Advt. No. IIA/16/2024-25 Dated: 21.08.2024

Indian Institute of Astrophysics (IIA) is an autonomous academic national institution under Department of Science & Technology, Govt. of India dedicated to research in Astronomy, Astrophysics and Allied Sciences & Technology. The Institute has its main campus in Koramangala, Bengaluru and CREST Campus at Hosakote, Bengaluru. It operates field stations at Kavalur & Kodaikanal in Tamil Nadu, Gauribidanur in Karnataka, and Leh/Hanle in Union Territory of Ladakh.

Online applications are invited from eligible, young, bright and highly motivated individuals for the following post to work under STU project at Indian Institute of Astrophysics, Bengaluru.

01.	Name of the position	Project Engineer – I (Electronics)
02.	No. of Position	One (01)
03.	Age limit	30 years
04.	Remuneration	Rs. 75,000/- per month (Consolidated)
05.	Essential Education Qualifications & Experience	<p>Full time Post graduate degree of M.E/M.Tech in Electronics /Electronics and Communication Engineering/Electrical and Electronics Engineering/Instrumentation with 60% marks in the aggregate or equivalent grade from a reputed and recognized University/Institution under UGC or Central or State Governments with One (01) year of experience in FPGA based HDL development in a reputed Industry/Research Organization and having hands on experience in scripting FPGA based design in Verilog/VDHL, synthesis, simulation and test bench development using Vivado tool chain.</p> <p style="text-align: center;">OR</p> <p>Full time graduate degree of B.E/B.Tech in Electronics /Electronics and Communication Engineering/ Electrical and Electronics Engineering /Instrumentation with 60% marks in the aggregate or equivalent grade from a reputed</p>

		and recognized University /Institution under UGC or Central or State Governments with Three (03) years of experience in FPGA based HDL development in a reputed Industry/Research Organization and having hands on experience in scripting FPGA based design in Verilog/VDHL, synthesis, simulation and test bench development using Vivado tool chain.
07.	Desirable Qualification and Experience	1). Experience on FPGA hardware design, testing and troubleshooting and PCB Schematic design on Orcad is preferred. 2). Candidates with higher qualification in relevant field and experience will also be considered.
08.	Place of posting	IIA, Bengaluru

Job Description: To be a part of instrument development team that handles design, fabrication, calibration and testing for ground and space-based astronomy experiments. Candidate should not have any objection to travel to field stations of IIA when required.

Terms & Conditions:

1. The appointment is purely temporary on contract basis and does not confer any privilege or benefit applicable to regular employment. No claim whatsoever for regular employment in the Institute shall be entertained.
2. The tenure of appointment is initially for a period of one (01) year and extendable annually subject to satisfactory performance of the candidate assessed annually and requirement of the project or may be co-terminus with completion of the project whichever is earlier.
3. The date of determining the upper age limit, qualifications and experience shall be the closing date prescribed for receipt of completed applications.
4. It is open to the Institute to conduct written test to shortlist the candidates for interview in case the attendance of candidates is more.
5. Age relaxation is permissible to SC, ST & OBC (Non-Creamy Layer) candidates and also to physically handicapped candidate as notified by Government of India from time to time.
6. The incumbent candidate will be posted to work in IIA, Bengaluru and is liable to be posted anywhere in India as per the requirement of the Institute/Project.
7. NO TA/DA will be reimbursed to the shortlisted candidates for attending Interview / Skill Test.
8. No correspondence will be entertained with the candidates not selected for interview/appointment. Canvassing in any form will be a disqualification.
9. Candidates of Indian Nationality only can apply for this post.
10. The prescribed educational qualifications are minimum required, and mere possession of the same does not entitle candidates to be called for interview. Where the number of applications received in response to the advertisement is large and it will not be convenient or possible for the Institute to interview all those candidates, the Institute reserves the right to limit number of candidates to be called for interview on the basis

of qualifications and experience higher than the minimum prescribed in the advertisement.

11. The Institute reserves the right to cancel the entire recruitment process at any time without assigning any reasons whatsoever.
12. The Institute reserves the right to increase/decrease the advertised vacancies subject to requirement of the project/institute.
13. **Candidates meeting the above requirements and willing to be considered for the above said post may submit application through online only.** Candidates are required to upload their scanned Curriculum Vitae (CV), Date of Birth Proof, Educational Qualification, Experience and Community Certificates in the online application failing which they will not be considered for further stages of scrutiny/recruitment process.
14. Misrepresentation or falsification of facts detected at any stage of the selection process or instances of misconduct/misbehaviour at any stage during selection process shall result in cancellation of candidature without any notice and no correspondence in this regard shall be entertained.
15. The last date for receipt of online application is 21.09.2024 at 17:30 hours.
16. For registration, please visit: https://www.iiap.res.in/iaa_jobs/ .

प्रशासनिकअधिकारी/ Administrative Officer