



**Indian Institute of Technology Gandhinagar
Palaj, Gandhinagar
Gujarat – 382355**

20/04/2020

Advertisement

Applications are invited from bright, young candidates for the following post under a DST funded project in Discipline of Electrical Engineering.

Name of Post : Junior Research Fellow (JRF)

Consolidated Monthly Pay : Rs. 31,000/- (consolidated)

Qualification and Experience : BE/BTech/ME/MTech in Electrical/Electronics and related disciplines with >75% marks (or > 8.0 CPI) and knowledge in Semiconductor Devices or Microelectronics or equivalent. Working knowledge of Technology CAD tools like Synopsis Sentaurus or Silivaco is required. Experience in semiconductor device modeling, device characterization and LINUX operating system will be preferred.

Tenure : 1 year extendable till end of project on satisfactory performance

Self-motivated students (fulfilling the above eligibility criteria), who are willing to work in the area of advanced semiconductor devices and technology are encouraged to send me a mail at (nihar@iitgn.ac.in) on or before **16th of May 2020**. The selection will be based on interview and shortlisted candidates will be intimated well in advance the date and time of the interview.

Queries may be directed to Dr. Nihar Ranjan Mohapatra (Professor, EE, IIT Gandhinagar).