

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE**  
(Department of Electrical Engineering)

Dated: 17/01/2025

**ADVERTISEMENT TO FILL UP PROJECT POSITIONS\***

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal Investigator, **Prof. Manoj Tripathy**, Department of Electrical Engineering, Indian Institute of Technology Roorkee.

1. Title of project: **Cyber Resilient Framework for Grid Edge Connected Distributed Energy Resources**
2. Sponsor of the project: **CRG/SERB, DST**
3. Project position(s) and number: **JRF (01)**
4. Qualifications: Requirement of **B.Tech./B.E./M.E./M.Tech. (Electrical/ Energy Science & Renewables/ EV/Instrumentations) With GATE** (Desirable Background: *Knowledge of Automation/ Cybersecurity in Microgrid*)
5. Emoluments: **Rs. 37,000/-+HRA PM** (as per GoI Norms)
6. Duration: **03 Years or till the end of project (whichever is earlier)**
7. Job description: *Set up cyber-physical hardware, including various types of DER and communications, and develop, validate and test IDS algorithms against cyber intrusions and allow secure integration of DERs into the distribution grid.*
8. Candidates, before appearing for the interview, shall ensure that they are eligible for the position they intend to apply for.
9. Candidates desiring to appear for the Interview should submit their applications with the following documents to the Principal Investigator **through email only** (manoj.tripathy@ee.iitr.ac.in).
  - Application in plain paper with a detailed CV including chronological discipline of degree/certificates obtained.
  - Experience including research, industrial field, and others.
  - Self-attested copies of degree/certificate and experience certificate.
  - **Proof of qualifying GATE score** (including marks obtained, rank and year of qualification).
10. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
11. Preference will be given to SC/ST candidates on equal qualifications and experience.
12. Please note that no TA/DA is admissible for attending the interview.
13. The last date for application to be submitted to the Principal Investigator through email is **February 28, 2025, by 5:00 PM**. Only short-listed candidates will be called/informed for the interview.
14. The schedule/mode of the interview will be communicated separately through email.

**Note:** The selected candidate may get an opportunity to pursue Ph.D. (Once the project is over, he/she will be given MHRD fellowship as per institute Policy)

Tel: 01332-285431

Email: manoj.tripathy@ee.iitr.ac.in

Copy forwarded for information and wide publicity.

1. Dean SRIC
2. Institute Website IIT Roorkee

Approved  
 (Signature)  
 डीन (सिक)/(Dean SRIC)  
 भा. प्रौ. सं. रुड़की/IIT Roorkee

(Dr. Manoj Tripathy)  
Principal Investigator

Digitally signed by Prof. Manoj Tripathy  
 DN: cn=Prof. Manoj Tripathy, o=IIT Roorkee, ou=Department of Electrical Engineering, email=manoj.tripathy@ee.iitr.ac.in, c=IN  
 Date: 2025.01.17 18:54:06 +05'30'

\*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.