



इलेक्ट्रॉनिक्स एवं संचार अभियंत्रण विभाग
ELECTRONICS AND COMMUNICATION ENGINEERING DEPT.
 राष्ट्रीय प्रौद्योगिकी संस्थान पटना / NATIONAL INSTITUTE OF TECHNOLOGY PATNA
 अशोक राजपथ, पटना- 800 005 / ASHOK RAJPATH, PATNA - 800 005, BIHAR

Cr291

संदर्भ/ Ref.: ECBIBINT/16/26

दिनांक/ Date: 03.02.2026

Recruitment of Junior Research Fellow (JRF) in the Department of Electronics and Communication Engineering

Applications are invited from Indian nationals for the following position in the project entitled “**Design and development of Audio Sensor and Data Acquisition Systems**” for the consortium project “**ML-Enabled RISC-V based i-LoRa SOC for Forest Event monitoring**” by Ministry of Electronics and Information Technology (MeitY), under Chips to Start-up (C2S) Programme, Govt. of India, New Delhi.

Name of the Position	Junior Research Fellow (JRF)
Number of Posts	02 (Two)
Duration of the Post	02 (Two) Years, thereafter Senior Research Fellow (SRF) till completion of the project
Consolidated Fellowship/Salary	Rs. 37,000+HRA @ 18% per month (for JRF) Rs. 42,000+HRA @ 18% per month (for SRF)
Essential Qualification	B.E./B.Tech. with minimum of 60% marks in Electronic and Communication Engineering with valid GATE score. (or) M.E./M.Tech. with minimum of 60% marks in Electronics and Communication Engineering/VLSI/Embedded System/ Microelectronics/ Electronics System and GATE qualified Preferable.
Desirable Requirements	(i) Knowledge of OS (Window/Linux/CentOS/RHEL). (ii) Knowledge of EDA tools (Cadence/Synopsys/Xilinx Vivado). (iii) knowledge of Analog/Mixed-mode Circuit Design, HDL Coding.

How to Apply:

Interested and eligible candidates may send the scanned copy of the application form (annexure-I) along with resume highlighting the relevant exposure and scanned copy of all testimonials to the mail id: balchandnagar@nitp.ac.in on or before **27th Feb 2026**.

Note:

1. Position is purely temporary and is coterminous with the project.
2. An applicant has to ensure the authenticity of the information provided in support of experience claimed, other documents and photograph (ID proof).
3. Shortlisted candidates and the date of the interview will be notified via email.
4. No-Objection certificate has to be submitted by the candidates who are already employed.
5. No TA/DA will be paid for attending the interview.
6. The candidate is expected to join immediately if selected.
7. The selected candidates shall be encouraged to register for Ph.D. in Electronics and Communication Department during the upcoming session.

Balchandnagar

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