

NATIONAL INSTITUTE OF TECHNOLOGY, ROURKELA – 769 008 (ODISHA) SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION

Advt. No.: NITR/SR/ EC-SMP /2020/WI/451 Date: 02.03.2020

Walk-in Interview will be conducted for following assignment in a time bound research project (Code:EC-SMP) in the **Dept. of Electronics & Communication Engineering, NIT Rourkela on 20th March, 2020 and Time 10.00 A.M.**

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1.	Name of the temporary post & No.	Guest Faculty	01 Post
2.	Name of the Research Project	Special Manpower Development Programme for Chips to System Design (SMDP-C2SD)	
3.	Name of the Sponsoring Agency	Government of India, Ministry of Communications & Information Technology, Department of Electronics & Information Technology, New Delhi – 110 003	
4.	Tenure of the Project and	From 15 DEC, 2014 to 31 DEC, 2020	
5.	Tenure of the Assignment	Appointment will be made up to 31 DEC 2020 or till project completion based on performance and institute guidelines.	
6.	Job Description (Give details)	Taking care of the VLSI Lab including software installation, upgradation.	
7.	Consolidated monthly Fellowship	Rs.40,000/-	
8.	Essential Qualification and Experience	1 st Class M. Tech. or equivalent in specialization VLSI Design / Microelectronics / Embedded System with B. Tech. degree in ECE / EI / EE / CS related areas with hands on experience on VLSI EDA / TCAD tools and knowledge of Linux operating system and server maintenance.	
9.	Desirable Qualification / Experiences (Higher qualifications & other experiences)	Familiarity with EDA tools like Synopsis / Cadence. Teaching and / or Research experience in the field of VLSI Design for a period of 3 years.	
10.	Trade Test	Not Required	
11.	Project Code	EC-SMP	
	Trade Test	Not Required	

Candidates are advised to report at 10:00 AM for Walk-in-Interview on 20th March, 2020 (Friday) in ECE Department.

If any queries, contact Prof. K. K. Mahapatra, Professor & Head, ECE Department & Chief Investigator, SMDP-C2SD at his e-mail id kkm@nitrkl.ac.in.

Important Information:

Candidates are advised to bring the following documents before appearing the interview.

- i) Two passport size color photographs.
- ii) CV CV should include contact details containing Present/Permanent Address, Phone No./Mobile No., e-mail ID, Date of Birth, Qualifications and Experiences, Project/Research Work and other relevant information.
- iii) Photo copies (One Set) of all certificates, mark sheets, experience certificates etc. along-with ORIGINAL documents for verification.
- iv) No TA/DA will be paid for appearing in the interview.
- v) Personnel appointed against this project post would get an opportunity to pursue Ph.D. / M. Tech. (Research) programme, subject to fulfilling the Institute criteria.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name: Prof. K. K. Mahapatra, Professor

Address: Dept. of Electronics & Communication Engineering

National Institute of Technology, Rourkela

Odisha 769 008.

Tel. No.: +91-<u>0661-246 2454</u> (Office)

E-mail: kkm@nitrkl.ac.in

Date and Time for walk-in Interview: 20th March, 2020 at 10.00 AM.

Place of Interview: Department of Electronics & Communication Engineering, NIT Rourkela

Eligible persons should bring all the original certificates and mark sheets, filled application form in the prescribed format (available in the Institute Website) affixed with colored photographs to be submitted in duplicate along with photo copies of relevant certificates, grade/ mark sheets, publications etc.. at the time of interview.

Sd/-Asst. Registrar (SRICCE)

Copy to:

- 1) All Heads of the Departments, NIT, Rourkela for publication on Departmental Notice Boards.
- 2) Prof. K. K. Mahapatra, PI with a request to give wide publicity to advertisement.
- 3) Head of the Department, EC.
- 4) Project file.